

SM2258 Flash F/W & ISP Release Information – R1008A

Introduction

This purpose of this document is to provide release information on the SM2246EN F/W and ISP release information

Fix Coverage

■ stands for the “new fix” or “new support” in the category

□ stands for the “no-update” in the category

■ Tester FW	■ Controller ISP
<ul style="list-style-type: none"> □ Yield Issue ■ Flash Issue <ul style="list-style-type: none"> □ SLC Flash <ul style="list-style-type: none"> □ Samsung Flash □ Toshiba/Sandisk Flash □ Intel/Micron Flash □ Hynix Flash □ Others ■ 3D TLC Flash <ul style="list-style-type: none"> ■ Samsung Flash □ Toshiba/Sandisk Flash □ Intel/Micron Flash □ Hynix Flash □ Compatibility issue □ Tester Bug Fix ■ AP Bug Fix & New Function ■ Feature Enhance 	<ul style="list-style-type: none"> ■ Yield Issue ■ Flash Issue <ul style="list-style-type: none"> □ SLC Flash <ul style="list-style-type: none"> □ Samsung Flash □ Toshiba/Sandisk Flash □ Intel/Micron Flash □ Hynix Flash □ Others ■ 3D TLC Flash <ul style="list-style-type: none"> ■ Samsung Flash □ Toshiba/Sandisk Flash □ Intel/Micron Flash □ Hynix Flash □ Compatibility issue ■ ISP Bug Fix □ Feature Enhance

ISP Revision History

Version	MP Tool version	ISP version	Note
R1008A	R0911A	R1008A	<ol style="list-style-type: none"> 1. Fix Samsung 3D V4 fake program fail issue. 2. Fix Samsung 3D V4 not disable fw boot code mode issue 3. Fix Dram ECC initialzatoion issue 4. Fix enhanced security erase without AES
R0810A	R0418A	R0810A	<p>FW does not support TCG/Opal.</p> <ol style="list-style-type: none"> 1. Add timer to meet Ulink protocol 6.0 Power 2. Management Fix backup page issue in the procedure of write protect block 3. Fix the issue of displaying "remained life percentage" in SMART 4. Fix erase count issue when page not align to 2^n 5. Fix the issue of "Hynix FW boot code mode" that cannot be disabled 6. Fix the issue of "Train ADJ NAND reset" 7. Add Hynix 3D v3 retry type "72" for same id different retry sequence
R0605C	R0418A	R0605C	<ol style="list-style-type: none"> 1. Fix the issue not saving info block in markbad flow 2. Add Winbond DDR 375 MHz setting (ISP and BootISP) 3. Modify Winbond DDR 525 MHz setting (ISP and BootISP) 4. Fix the issue of RDT LED 5. Fix issue of SLC align super page 6. Fix the issue in SaveG2FMap: FW only save ID to plane 0 and plane1's spare but does not handle plane2 and plane3 for hynix V4 4 plane case 7. Fix overflow issue in the flow of re-build F2H mapping table 8. Fix issue of "not initial value at security erase" 9. Add compile option, reconfirm DII lock value in the function of setting flash clock 10. Fix issue of "full card SLC empty block" count condition in SMART

R0426C	R0418A	R0426C	<ol style="list-style-type: none"> 1. Fix Pretest Reference Run Time Bad Bug 2. Add Flash CLK Choose Feature in MP tool 3. Add "Avg erase count" to judge block type selection (back-ground calculation) 4. Add Winbond Dram setting 5. Add retry table (SandiskBics3_16G and SandiskBics3_64G) 6. Add DASP LED function 7. Add LED low active function
			<ol style="list-style-type: none"> 8. Fix the issue of "not saving info block in markbad flow"
R0305B	R0309A	R0305B	<ol style="list-style-type: none"> 1. Update Security Erase Code 2. Update Vth tracking method 3. DRAM training is supported 4. pSLC (total SSD in SLC mode, 1/3 capacity) is supported 5. TCG/Opal supported
Q1204A	Q1201A	Q1204A	<ol style="list-style-type: none"> 1. Fix SATA PHY setting 2. New thermal throttling: Decrease NAND clock && Check read/write flow
Q1107B	Q1122A	Q1107B	<ol style="list-style-type: none"> 1. Modify the pretest issue in SanDisk BiCS3
Q1107A	Q1026A	Q1107A	<ol style="list-style-type: none"> 1. Modify one customer's SMART requirements.
Q0915A	Q0814A	Q0915A	<ol style="list-style-type: none"> 1. First release version

Note:

1. F/W and ISP update is recommended.
2. History # is denoted by "Version-Date" .

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